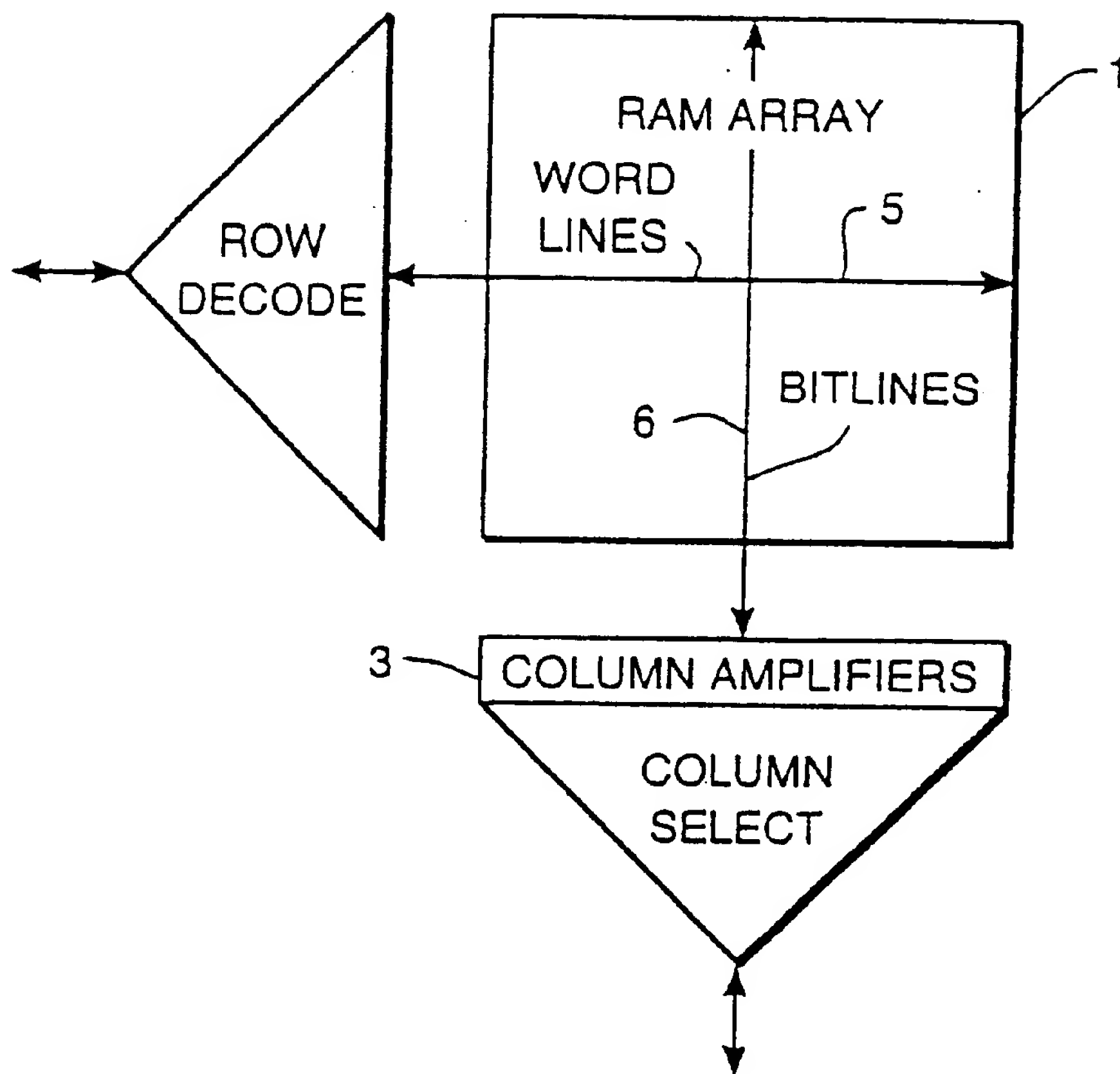




"Memory Device Having A Programmable Register"
Applicant: Farnwald, et al.
Application No. 09/801,151
Attorney Docket No. 57941.000026

FIG 1





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Attorney Docket No. 57941.000026

FIG. 2

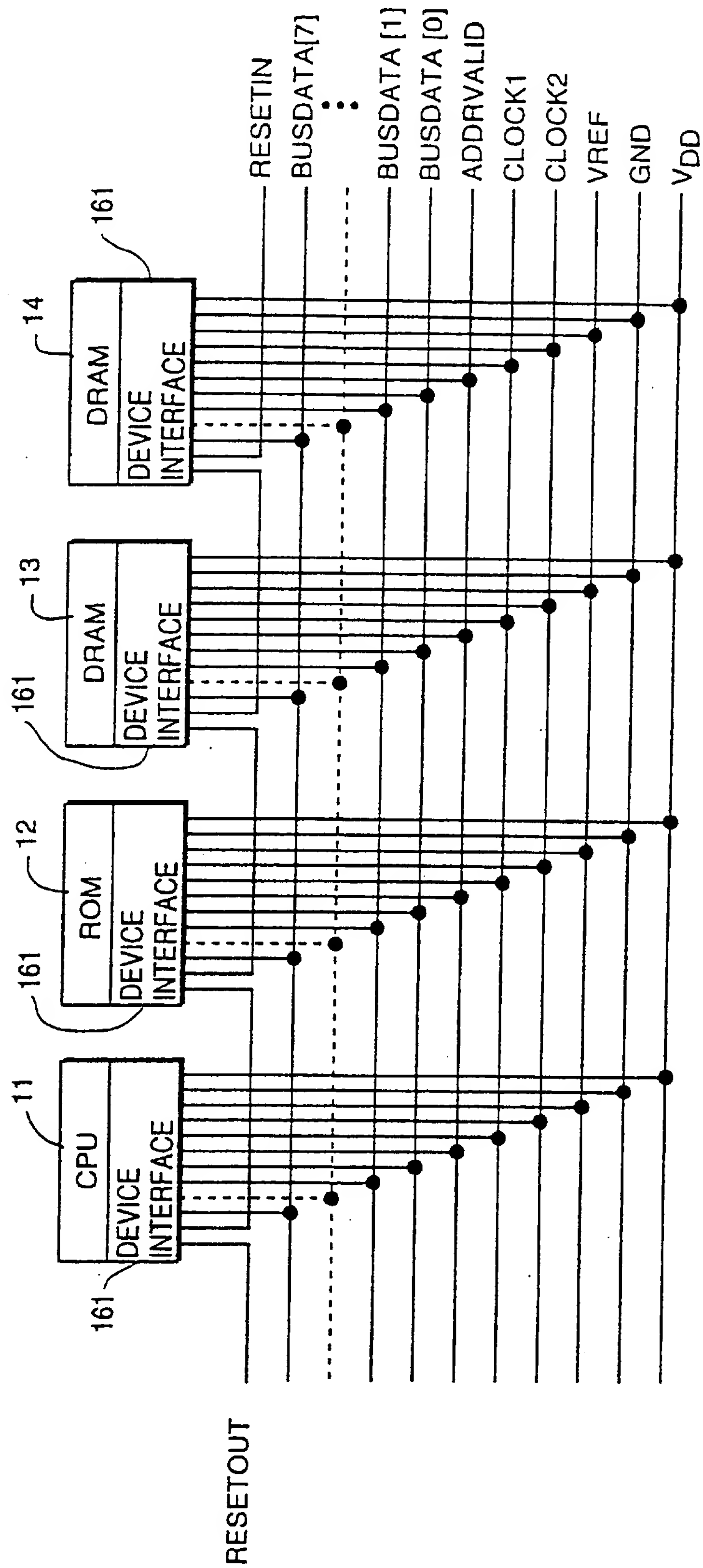
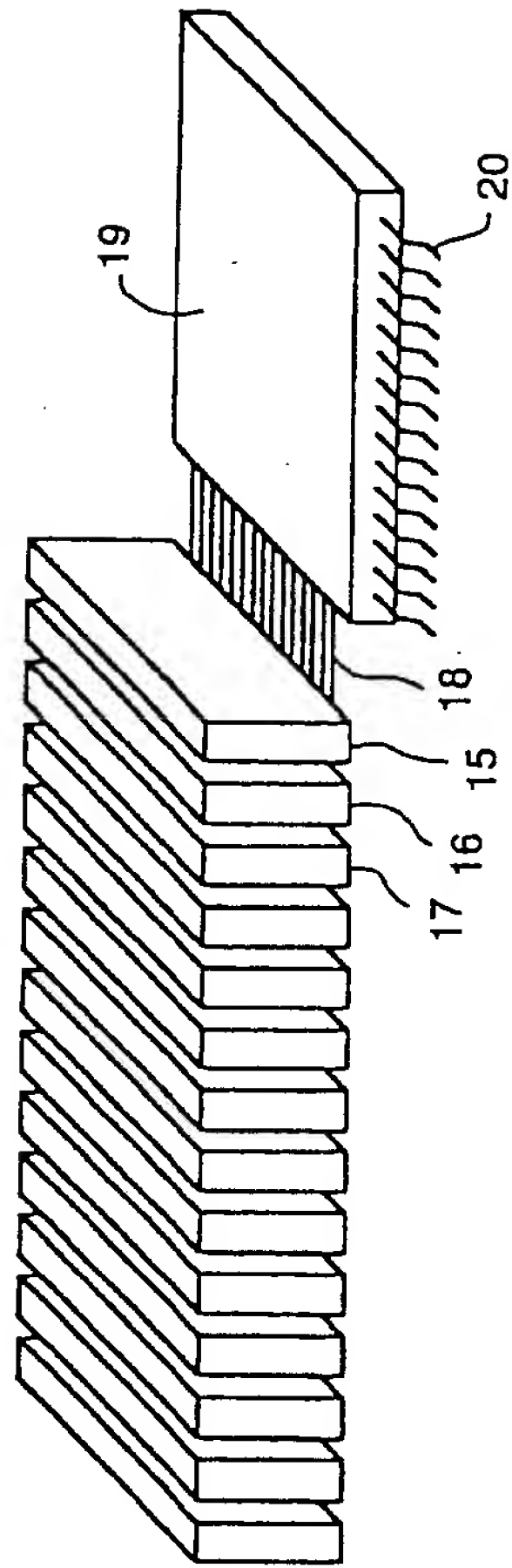


FIG 3



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REGULAR ACCESS

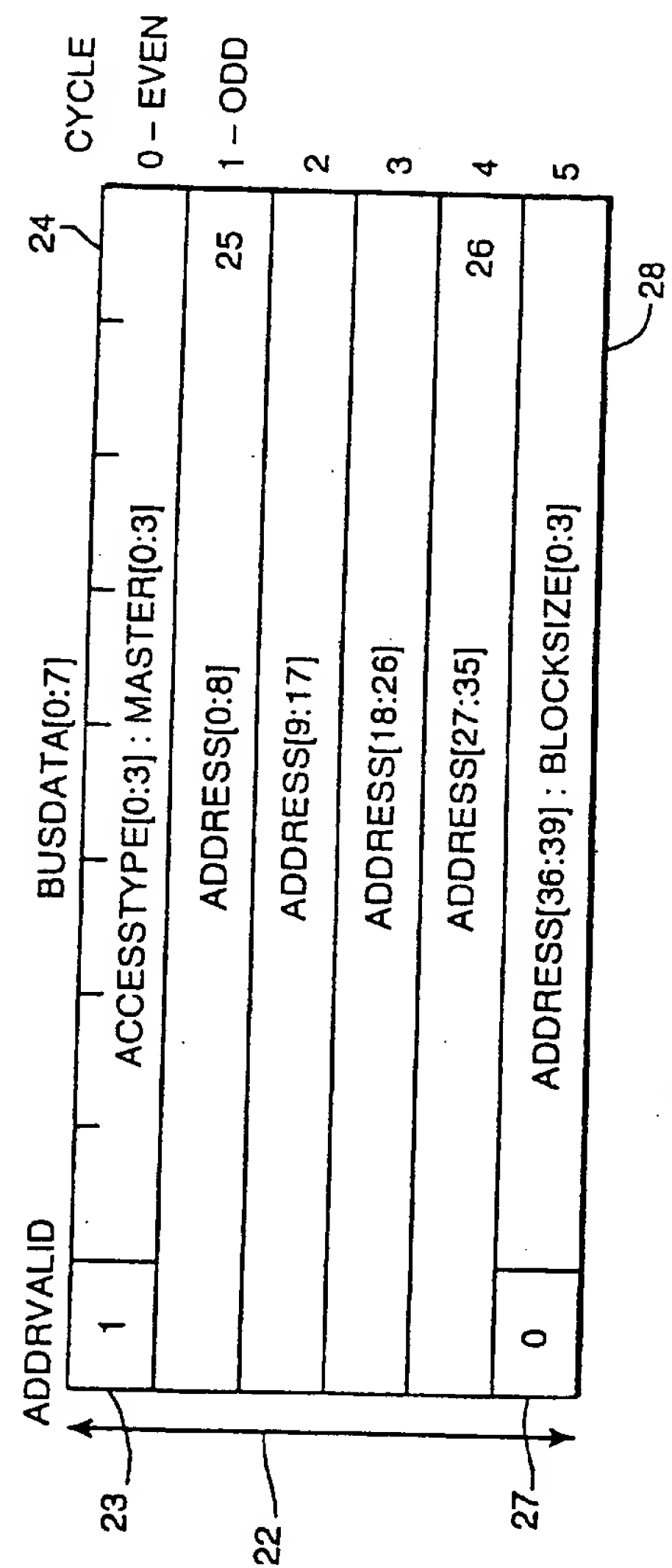


FIG 4



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REJECT (NACK) CONTROL PACKET

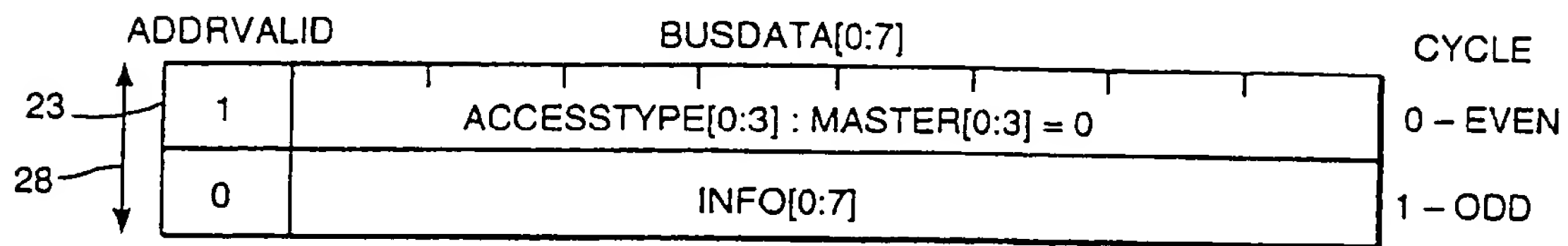


FIG 5

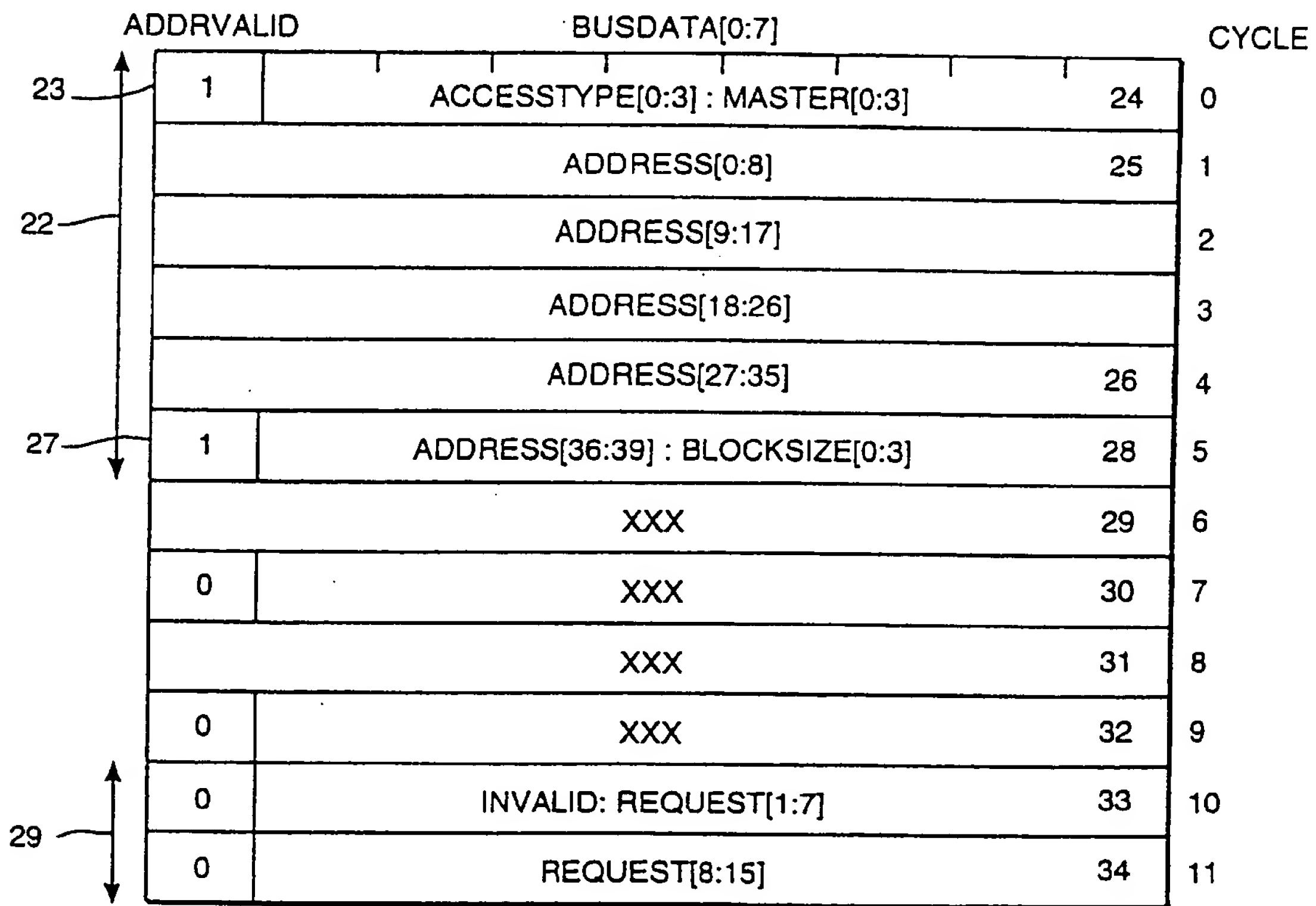


FIG 6



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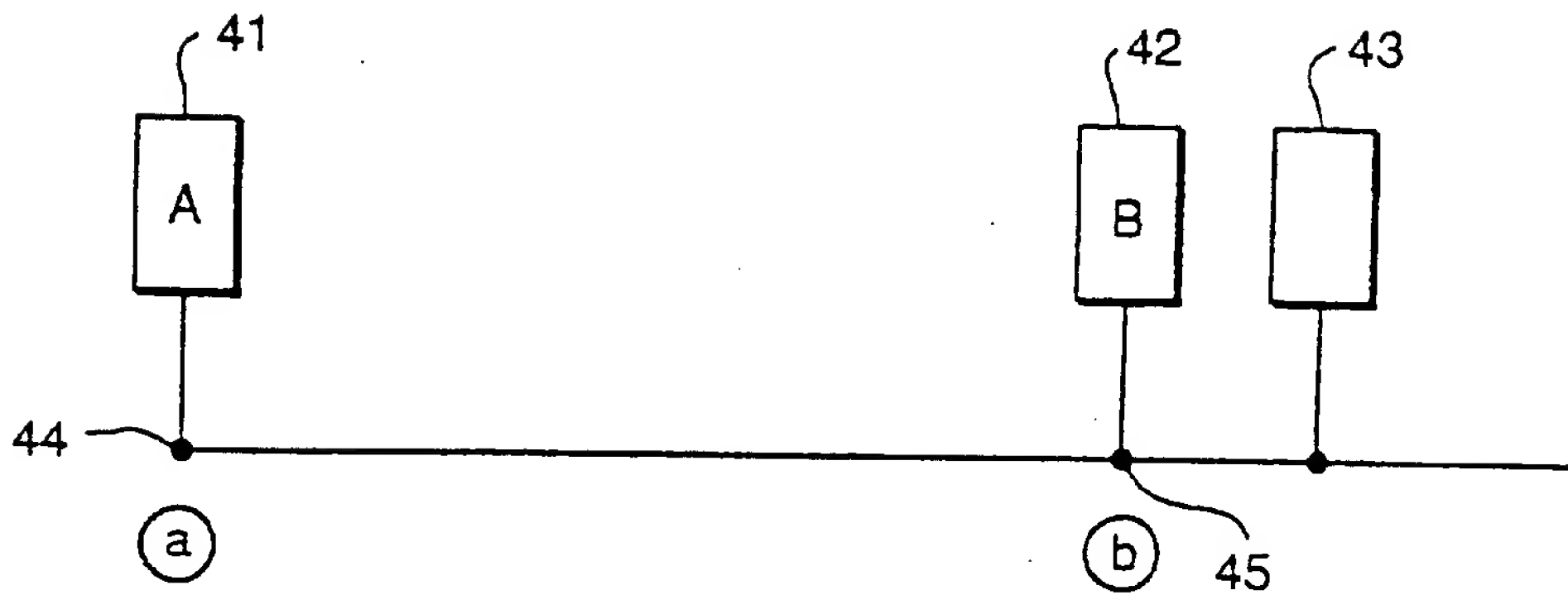


FIG 7A



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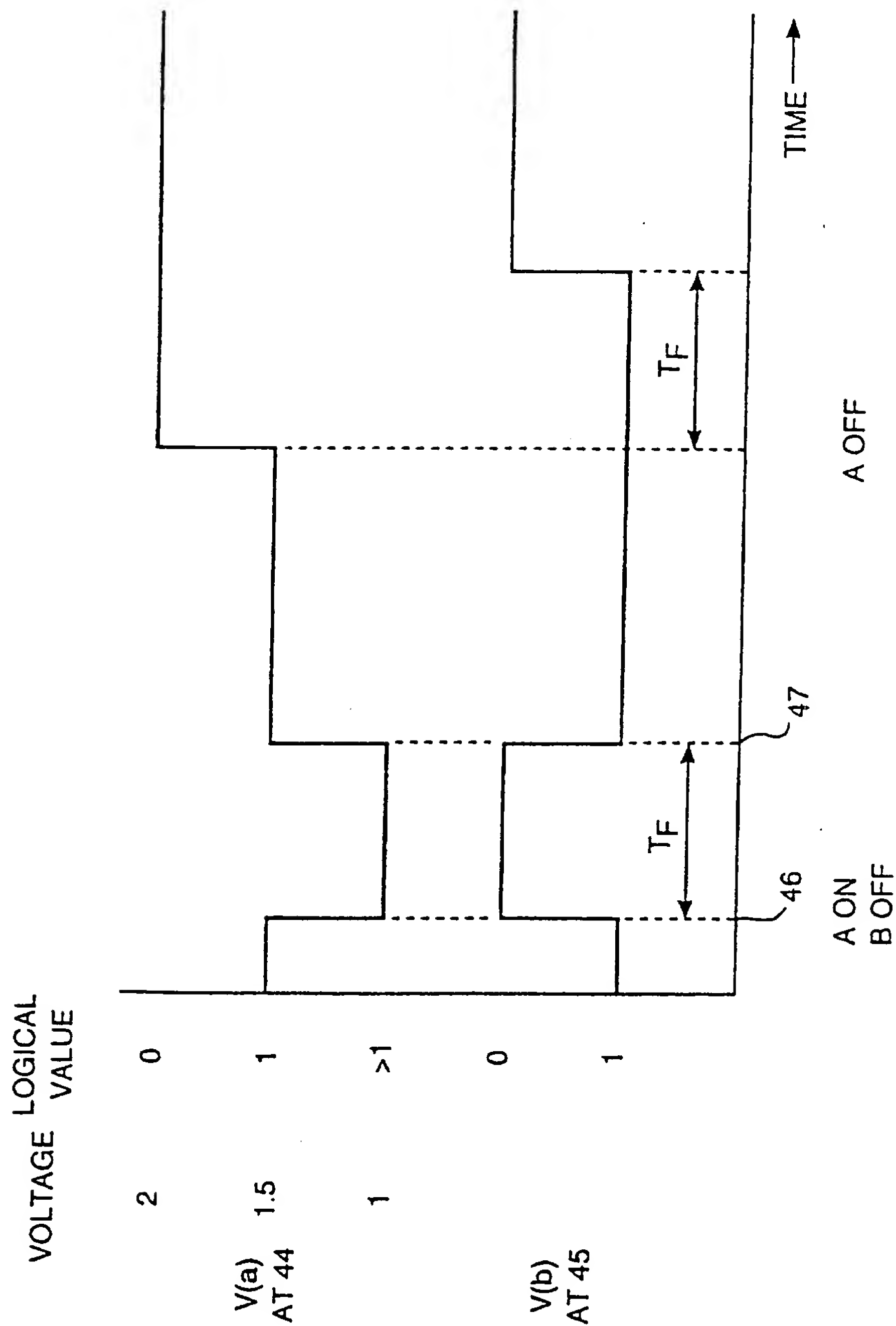


FIG 7B



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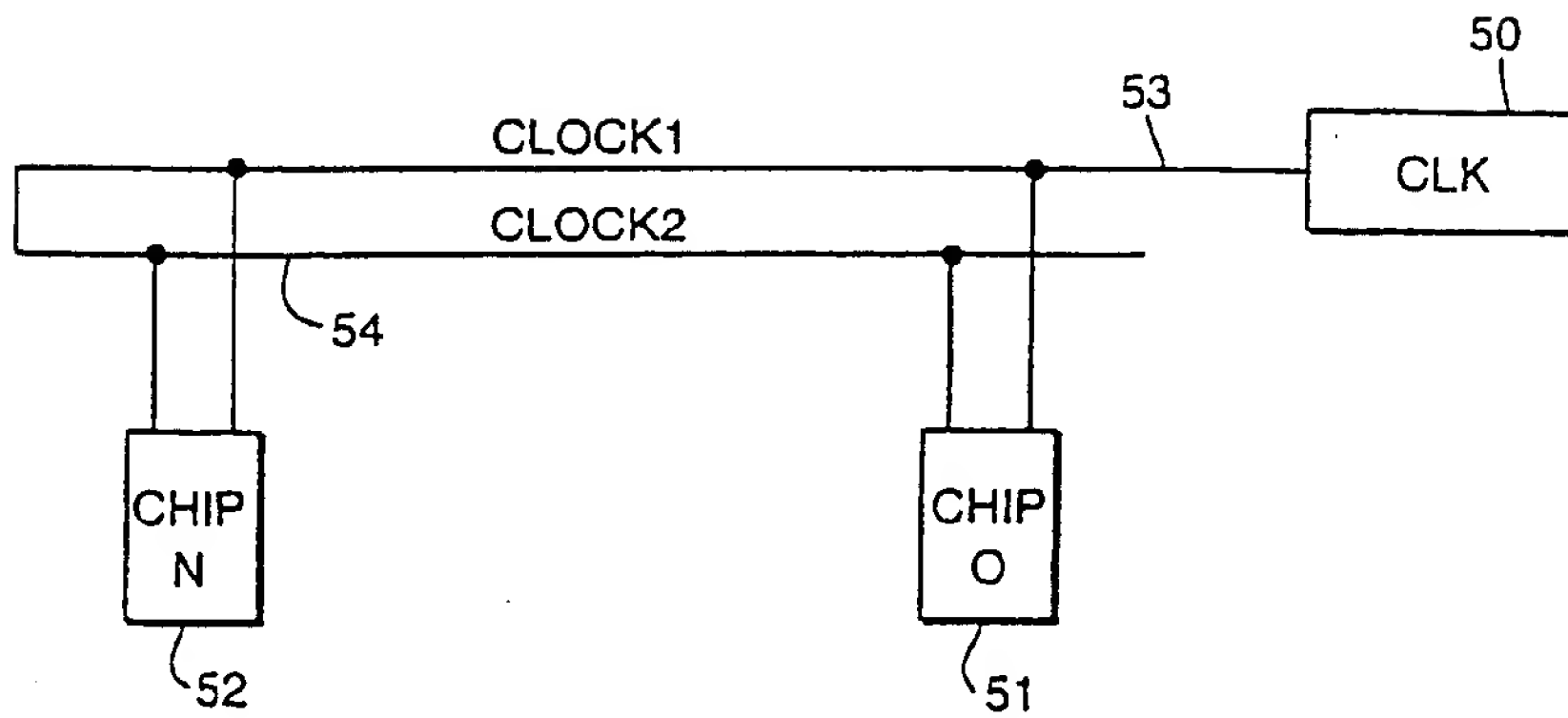


FIG 8A

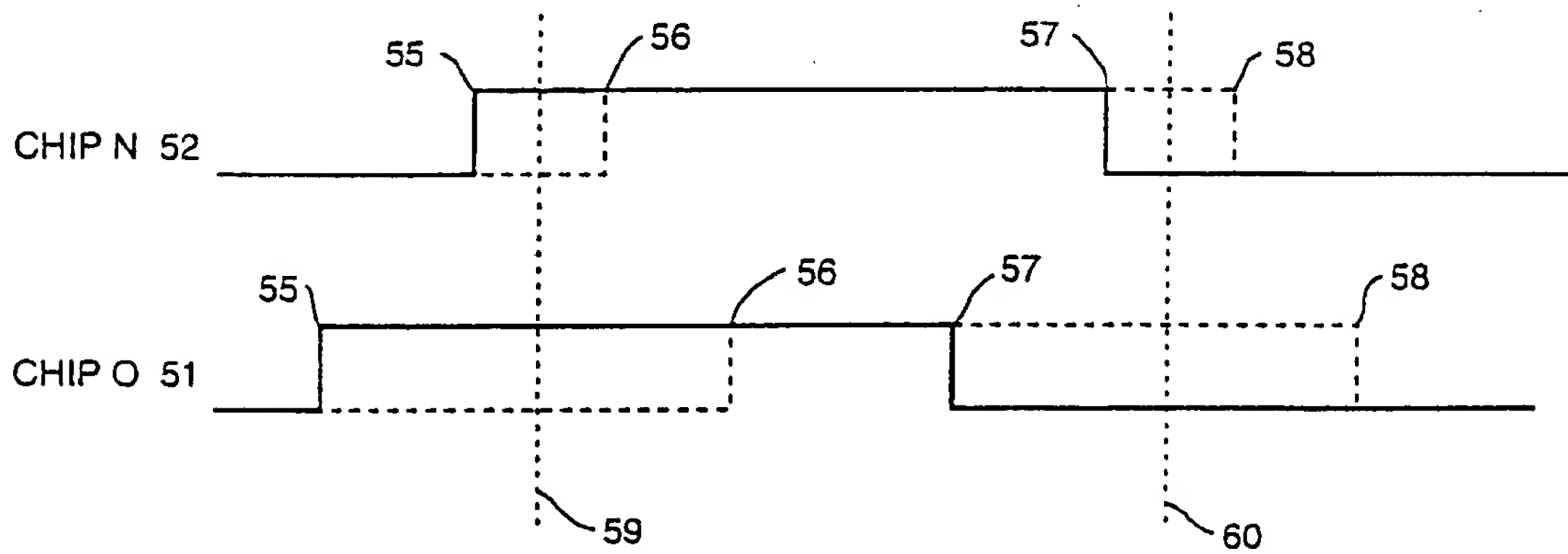


FIG 8B

THE

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Application No. 09/801,151

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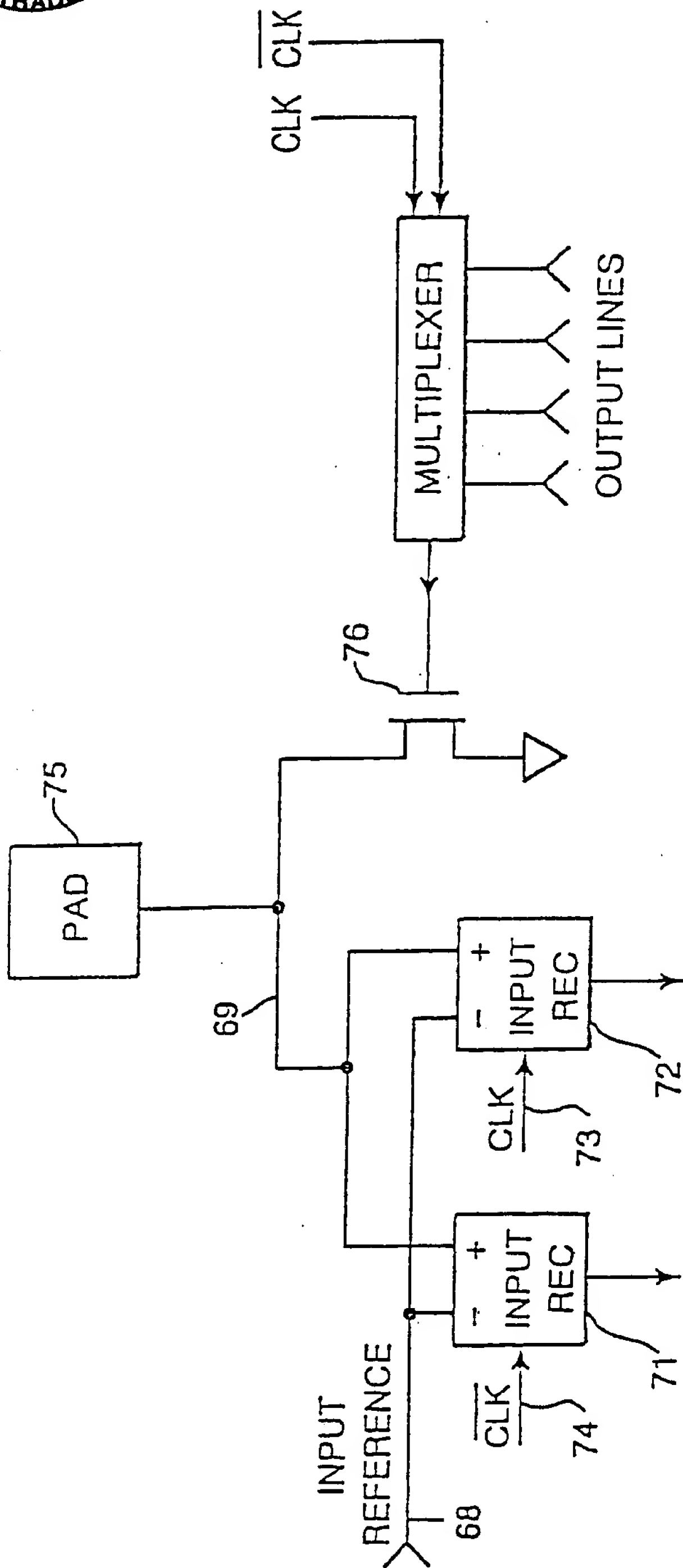
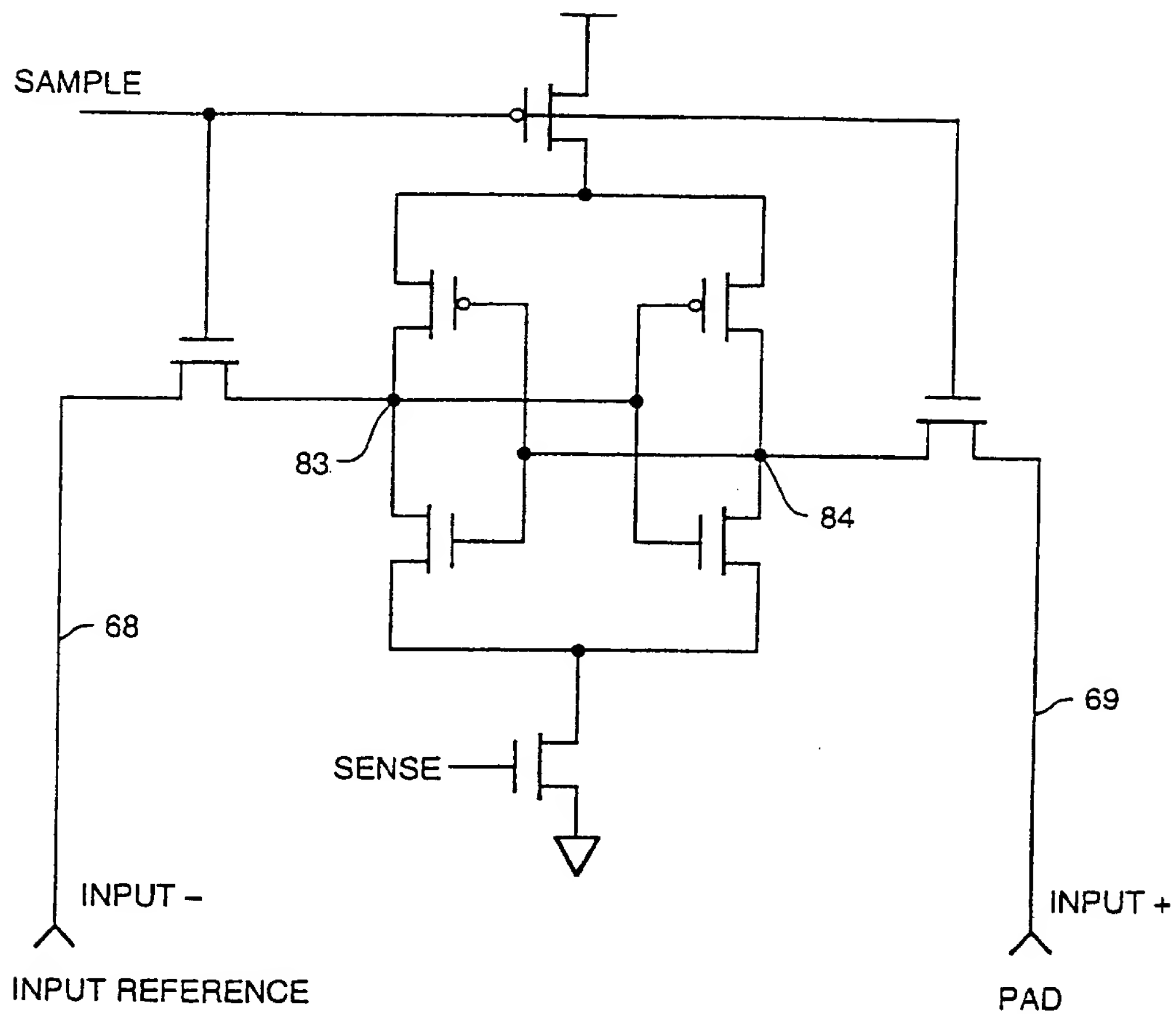


FIG. 10

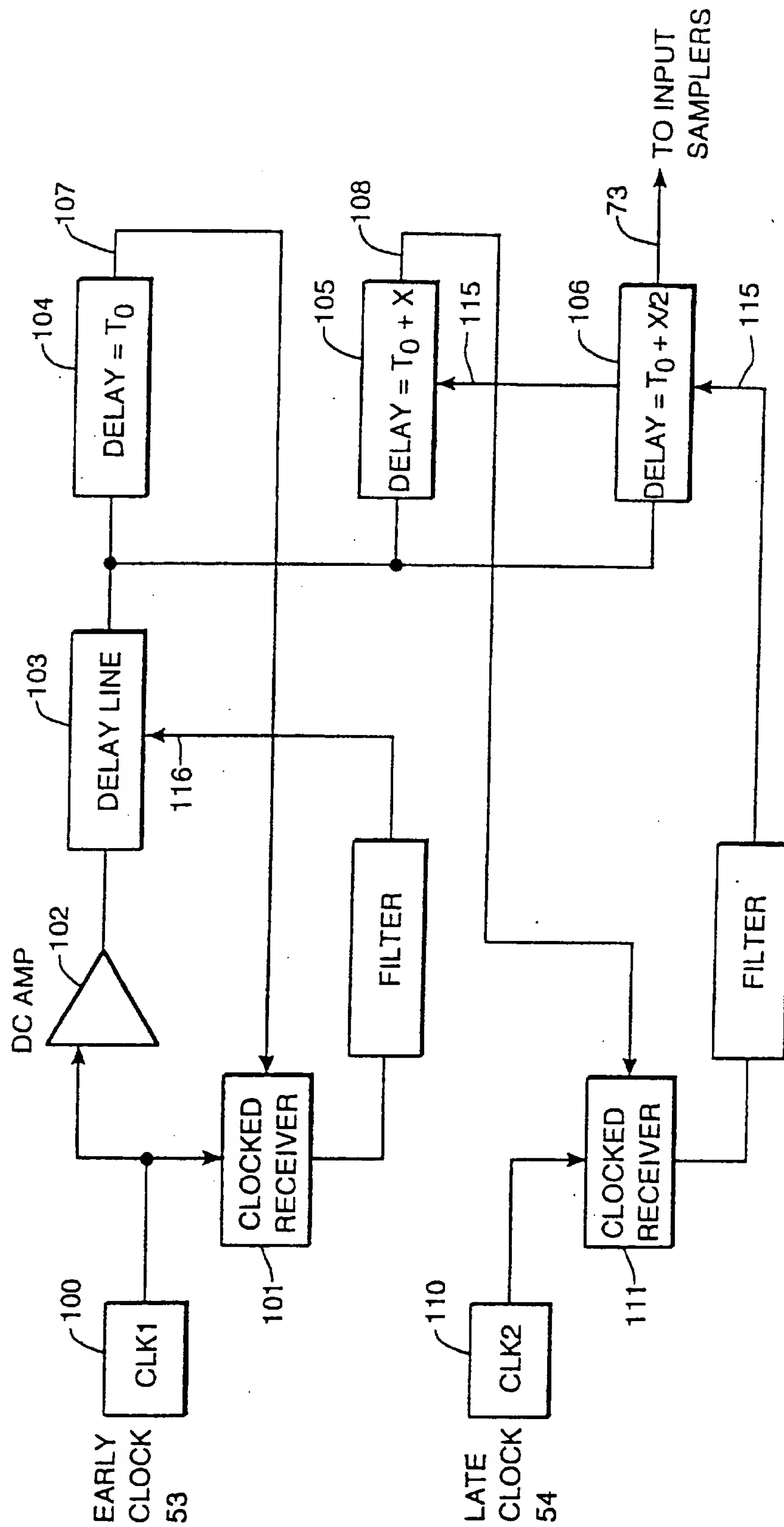


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FIG. 11



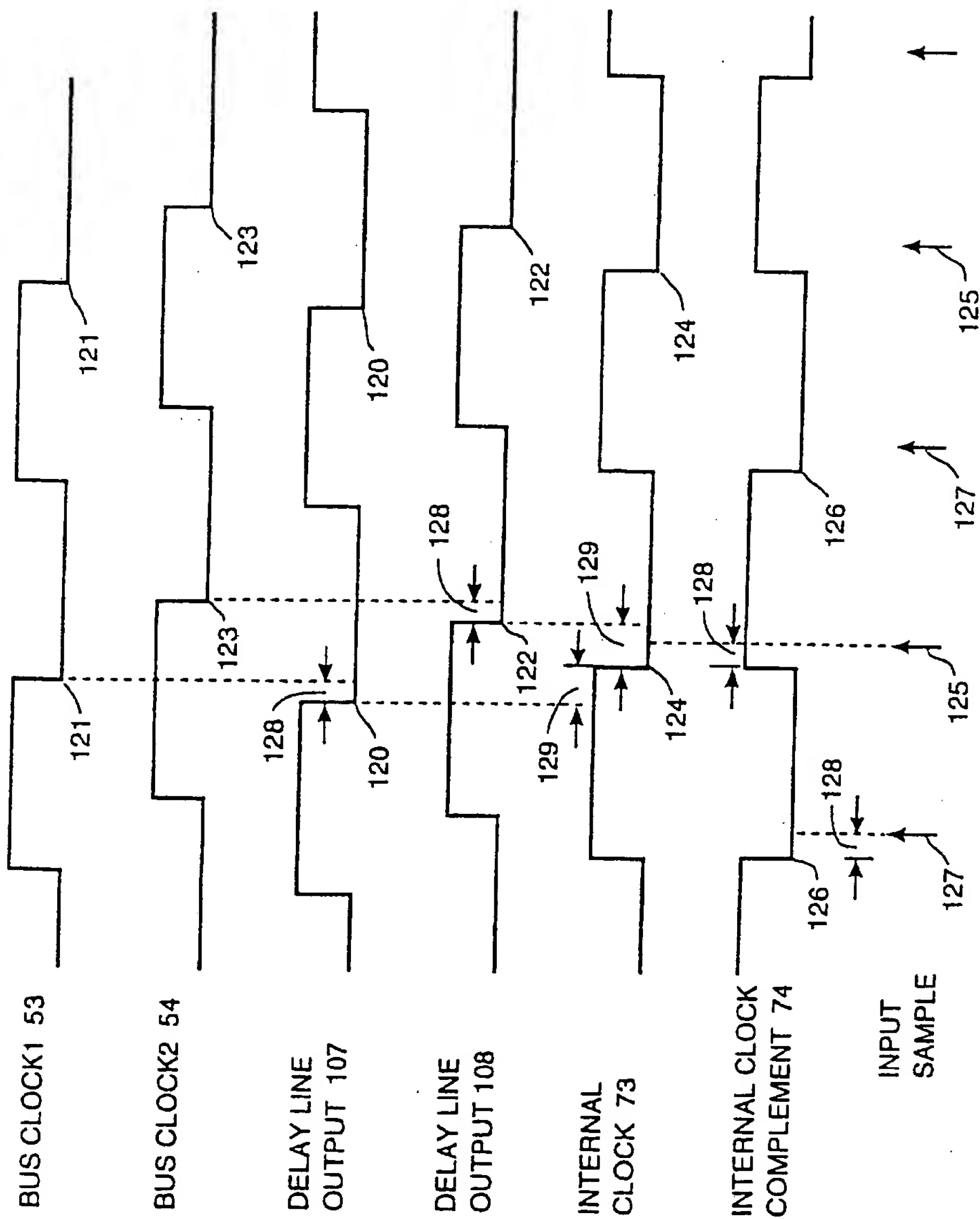
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Application No. 09/801,151
Attorney Docket No. 57941.000026

FIG. 13





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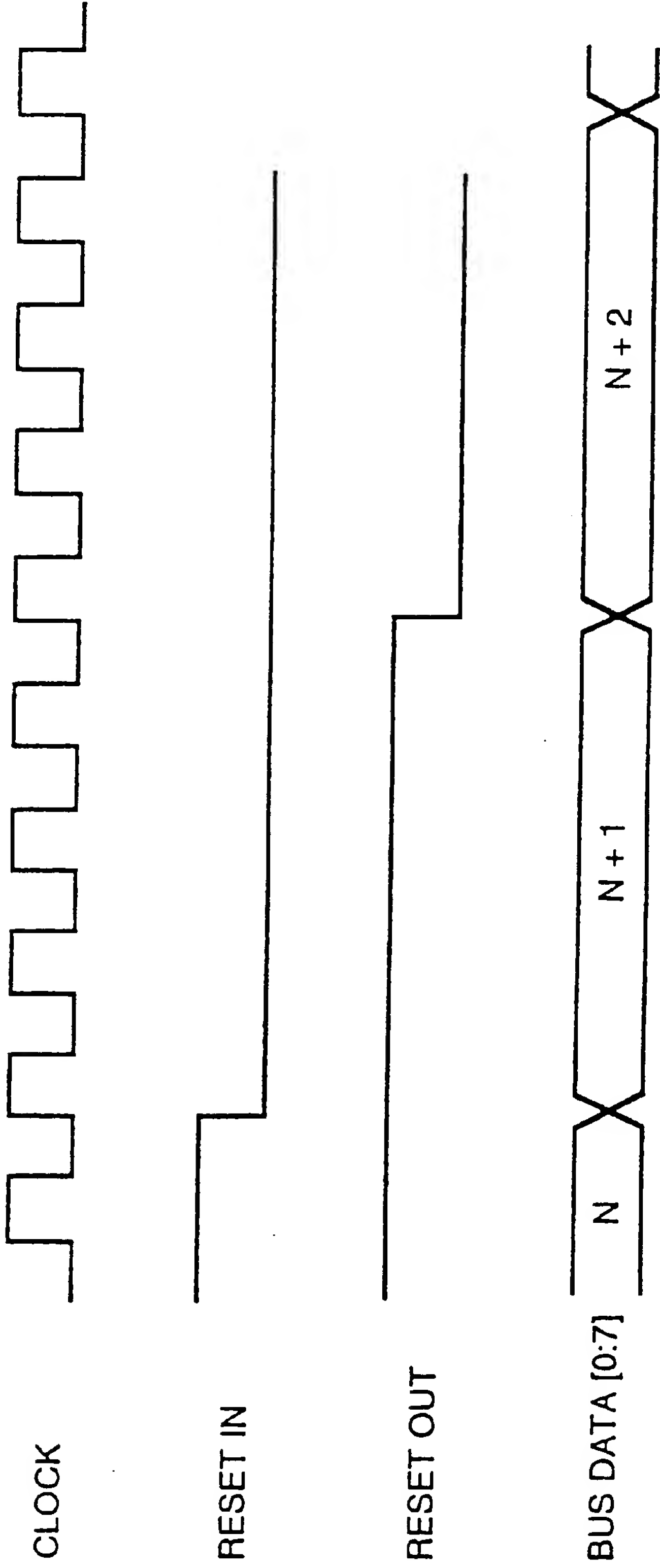
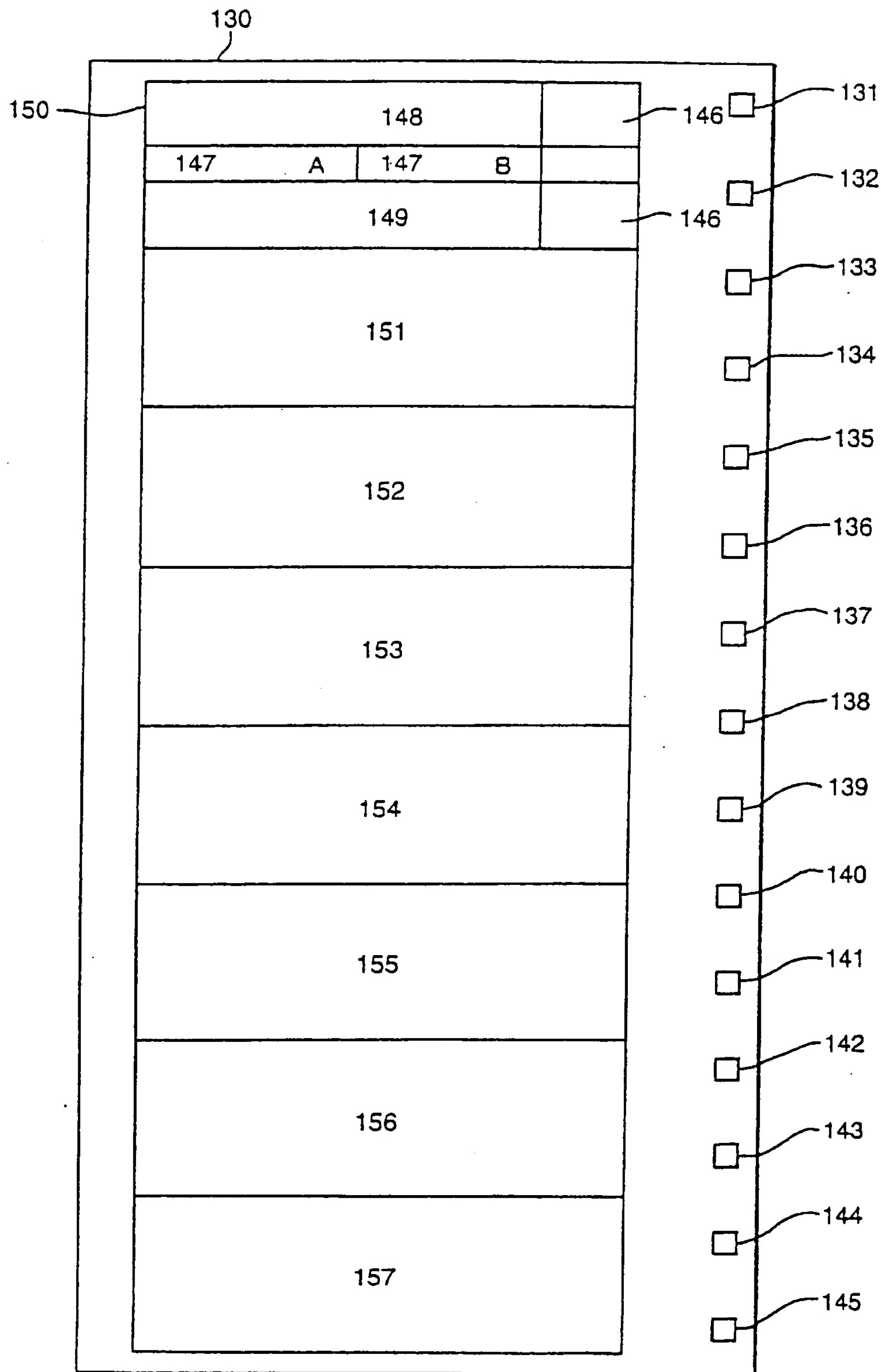


FIG. 14



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FIG 15





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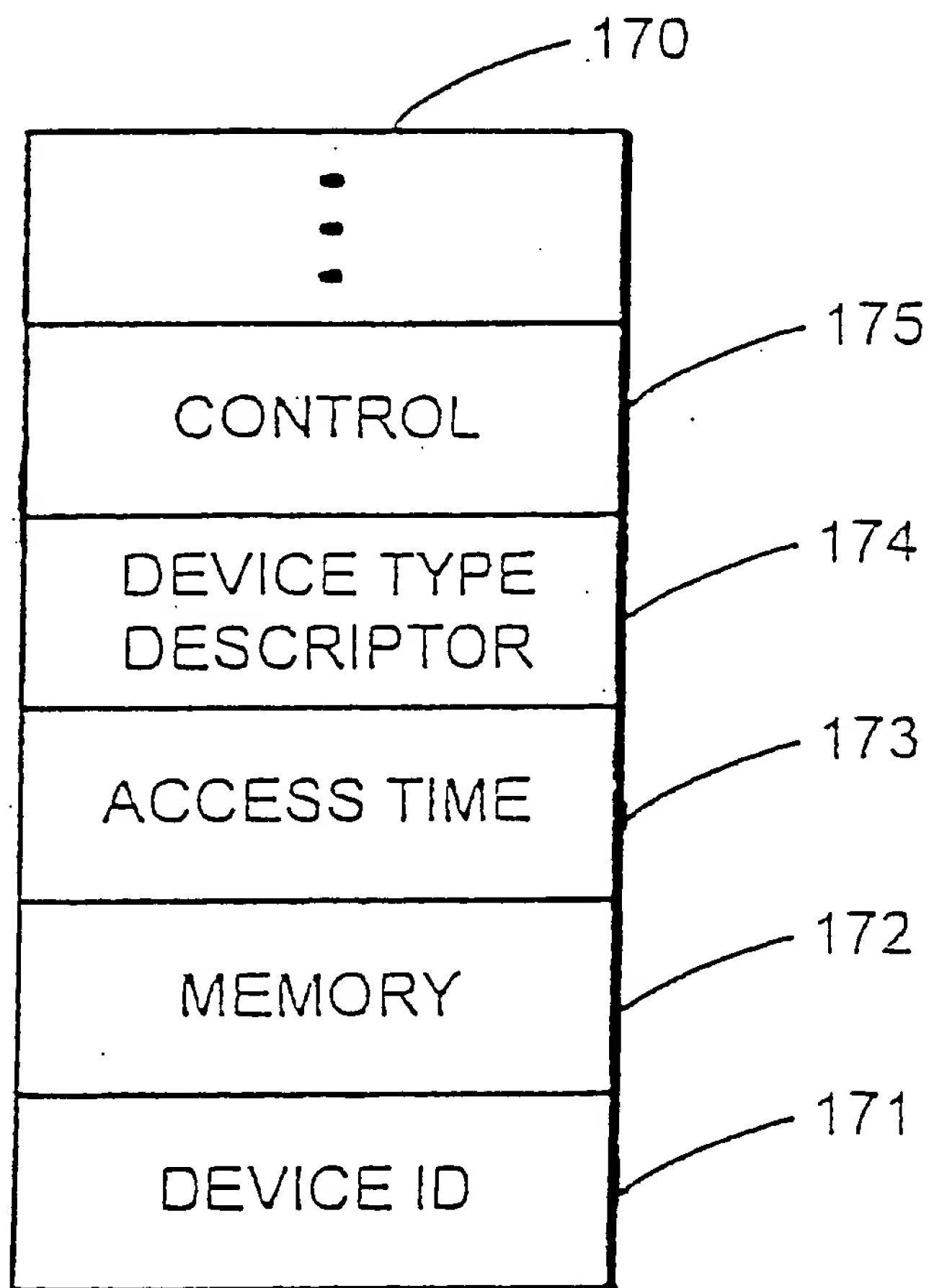


FIG. 16